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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
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10/058,854

01/28/2002

Kay Hellig

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02/04/2003

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EXAMINER

BREWSTER, WILLIAM M

ART UNIT

PAPER NUMBER

2823

DATE MAILED: 02/04/2003

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

10/058,854

Applicant(s)

HELLIG ET AL.

Examiner

William M. Brewster

Art Unit

2823

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 06 December 2002.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-14 is/are pending in the application.
- 4a) Of the above claim(s) 1-5 is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 6-14 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☒ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
- Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- 11) ☐ The proposed drawing correction filed on _____ is: a) ☐ approved b) ☐ disapproved by the Examiner.
- If approved, corrected drawings are required in reply to this Office action.
- 12) ☐ The oath or declaration is objected to by the Examiner.

Priority under 35 U.S.C. §§ 119 and 120

- 13) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.
- 14) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).
- a) ☐ The translation of the foreign language provisional application has been received.
- 15) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.

Attachment(s)

- | | |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413) Paper No(s). _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO-1449) Paper No(s) <u>5</u> . | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

Election/Restrictions

Claims 1-5 withdrawn from further consideration pursuant to 37 CFR 1.142(b), as being drawn to a nonelected invention, there being no allowable generic or linking claim. Applicant timely traversed the restriction (election) requirement in Paper No. 8.

Applicant's election with traverse of claims 6-14 in Paper No. 8 is acknowledged. The traversal is on the ground(s) that the restriction failed to point out the individual and distinct. This is not found persuasive because in Paper No. 3, paragraph bridging pages 2 and 3, the first source/drain region of claim one could be made using different species of ions at different times, whereas claims 6 and 14 specify using one specified of implant ions at the same time.

The requirement is still deemed proper and is therefore made FINAL.

Specification

The following guidelines illustrate the preferred layout for the specification of a utility application. These guidelines are suggested for the applicant's use.

Arrangement of the Specification

As provided in 37 CFR 1.77(b), the specification of a utility application should include the following sections in order. Each of the lettered items should appear in upper case, without underlining or bold type, as a section heading. If no text follows the section heading, the phrase "Not Applicable" should follow the section heading:

- (a) TITLE OF THE INVENTION.
- (b) CROSS-REFERENCE TO RELATED APPLICATIONS.
- (c) STATEMENT REGARDING FEDERALLY SPONSORED RESEARCH OR DEVELOPMENT.

- (d) INCORPORATION-BY-REFERENCE OF MATERIAL SUBMITTED ON A COMPACT DISC (See 37 CFR 1.52(e)(5) and MPEP 608.05. Computer program listings (37 CFR 1.96(c)), "Sequence Listings" (37 CFR 1.821(c)), and tables having more than 50 pages of text are permitted to be submitted on compact discs.) or REFERENCE TO A "MICROFICHE APPENDIX" (See MPEP § 608.05(a). "Microfiche Appendices" were accepted by the Office until March 1, 2001.)
- (e) BACKGROUND OF THE INVENTION.
 - (1) Field of the Invention.
 - (2) Description of Related Art including information disclosed under 37 CFR 1.97 and 1.98.
- (f) BRIEF SUMMARY OF THE INVENTION.
- (g) BRIEF DESCRIPTION OF THE SEVERAL VIEWS OF THE DRAWING(S).
- (h) DETAILED DESCRIPTION OF THE INVENTION.
- (i) CLAIM OR CLAIMS (commencing on a separate sheet).
- (j) ABSTRACT OF THE DISCLOSURE (commencing on a separate sheet).
- (k) SEQUENCE LISTING (See MPEP § 2424 and 37 CFR 1.821-1.825. A "Sequence Listing" is required on paper if the application discloses a nucleotide or amino acid sequence as defined in 37 CFR 1.821(a) and if the required "Sequence Listing" is not submitted as an electronic document on compact disc).

Currently, applicant has made the Brief Description of the Drawings what the Detailed Description of the Invention should be, but does not have a Detailed Description of the Invention title. Appropriate correction is required.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 6, 7, 12, 13 are rejected under 35 U.S.C. 102(b) as being anticipated by Lee et al., U.S. Patent No. 5,679,589.

Lee anticipates a method of fabricating a semiconductor transistor comprising the steps of: in fig. 2, providing a gate structure 17 having a sidewall portion and a top portion, said gate structure formed on a substrate 11; in fig. 3, limitations from claim 7: further including a step of forming a liner oxide 19 over said gate structure prior to the step of forming the dielectric spacer; forming a dielectric spacer formed over the substrate 21, in fig. 4, said dielectric spacer 21 forming an L-shape comprising a vertical portion parallel to the sidewall portion, and a horizontal portion approximately orthogonal to the sidewall portion of the gate structure; limitations from claim 12: the method of Item 6 wherein said L-shaped dielectric spacer is a nitride, col. 2, lines 42 - 57; , limitations from claim 13: wherein the length of the horizontal portion of the L-shaped dielectric spacer ranges from about 200 Angstroms to about 500 angstroms, col. 2, lines 42 - 57; in fig. 6, forming a first source/drain region 33 & 35, in the substrate using an implant species 37, wherein the first source/drain region formed underneath the horizontal portion of the L-shaped dielectric spacer; and forming a second source/drain region 25 & 27, in the substrate using the implant species, wherein the second source/drain region is immediately adjacent the first source, drain region and has a depth greater than a depth of the first source/drain region.

Claim 14 is rejected under 35 U.S.C. 102(b) as being anticipated by Zhou, U.S. Patent No. 6,156,598.

Zhou, in fig. 3, anticipates a method of fabricating a semiconductor transistor comprising the steps of: in fig. 1, forming a gate structure 22 having a sidewall portion

and a top portion, said gate structure formed on a substrate; in fig. 2, forming a dielectric spacer formed over the substrate, said dielectric spacer forming an L-shape comprising a vertical portion 47 parallel to the sidewall portion, and a horizontal portion 44 approximately orthogonal to the sidewall portion of the gate structure, in fig. 3, forming a source/drain extension having an average extension depth forming a first portion of a source/drain region 60 having a first average depth and a first length; forming a second portion of the source/drain region simultaneously in time with the first portion, wherein the second portion 62 has a second average depth and a second length, wherein the second average depth is greater than the first average depth, and the first average depth is greater than the average extension depth.

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

Claims 8-11 are rejected under 35 U.S.C. 103(a) as being unpatentable over Lee as applied to claims 6, 7, 12, 13 above, and further in view of Chen et al., U.S. Patent No. 6,074,915.

Lee does not specify the implant species or the conditions for implanting them, but Chen does. Chen in fig. 3H, forms a gate structure 165 having a sidewall portion and a top portion, said gate structure formed on a substrate; forming a dielectric spacer

180 formed over the substrate, forming a source/drain region by ion implanting As or BF_2 at an ion dose in the range of 1×10^{15} to 5×10^{15} ions/cm², at an energy of between about 10 to 50 keV. Chen gives motivation in col. 4, line 16 - 24. It would have been obvious to a person of ordinary skill in the art at the time the invention was made to recognize that combining Chen's process with Lee's invention would have been beneficial because it helps produce a salicide and SAC structures with a single masking step.

Conclusion

The prior art made of record and is considered pertinent to applicant's disclosure, Yu, U.S. Patent No. 6,506,650 B1. Applicant must state that the Assignee of Yu was commonly owned at the time of filing of the current application or Yu will be relied upon as a basis for rejection.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to William M. Brewster whose telephone number is 703-305-5906. The examiner can normally be reached on Full Time.


If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Olik Chaudhuri can be reached on 703-306-2794. The fax phone numbers for the organization where this application or proceeding is assigned are 703-305-3432 for regular communications and 703-305-3432 for After Final communications.

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Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is 703-308-0956.

WB
January 31, 2003


Olik Chaudhuri
Supervisory Patent Examiner
Technology Center 2800